Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended) An echo canceler for generating an echo replica and for subtracting the echo replica from a local input signal to create a residual signal for outgoing transmission, comprising:

a plurality of ~~adaptive filters~~processors which have different computational accuracy for generating the echo replica; and

~~an evaluator~~a controller coupled to said ~~adaptive filters~~processors for grouping coefficients into a plurality of segments, ~~and~~evaluating a computational accuracy information according to the coefficients for said segments,~~; and a controller in said evaluator for~~ assigning said local input signal into said ~~adaptive filters~~processors, and adjusting the coefficients according to the computational accuracy information.

Claim 2 (original) The echo canceler of claim 1, wherein the computational accuracy information is a power of the coefficients for each one of said segments.

Claim 3 (currently amended) The echo canceler of claim 1, wherein said controller assigns the local input signal to ~~an adaptive filter~~a processor for higher computational accuracy when the power for each one of said segments is above a predefined threshold.

Claim 4 (currently amended) The echo canceler of claim 1, wherein said controller

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assigns the local input signal to <u>a processor</u> ~~an adaptive filter~~ for lower computational

accuracy when the power for each one of said segments is below ~~the~~<u>a</u> predefined threshold.

Claim 5 (currently amended) The echo canceler of claim 1, further comprising a

supervisor for monitoring a computational overflow for each segment which is assigned to

said ~~adaptive filter~~<u>processor</u> for lower computational accuracy according to the computational

accuracy information, and ordering said controller to assign the local input signal to said

~~adaptive filter~~<u>processor</u> for higher computational accuracy when the overflow occurs in the

segment.

Claim 6 (currently amended) The echo canceler of claim 1, further comprising a

supervisor for monitoring a computational overflow for each segment which is assigned to

said ~~adaptive filter~~<u>processor</u> for lower computational accuracy according to the computational

accuracy information, and ordering said controller to assign the local input signal to an

additional ~~adaptive filter~~<u>processor</u> when the overflow occurs in the segment.

Claim 7 (currently amended) A method of canceling an echo component of a local

input signal to create a residual signal for outgoing transmission, comprising the steps of:

grouping coefficients into a plurality of segments;

evaluating a computational accuracy information according to the coefficients for

said segments;

assigning said local input signal to ~~said adaptive filters~~<u>a plurality of processors</u>

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having different computational accuracy according to the computational accuracy

information; and

adjusting the coefficients.

Claim 8 (original) A method according to claim 7, wherein said step of evaluating a

computational accuracy information is a power of the coefficients for each one of said

segments.

Claim 9 (currently amended) A method according to claim 7, wherein said step of

assigning a controller assigns the local input signal to ~~an adaptive filter~~a processor for higher

computational accuracy when the power for each one of said segments is above a predefined

threshold.

Claim 10 (currently amended) A method according to claim 7, wherein said step of

assigning said controller assigns the local input signal to ~~an adaptive filter~~ a processor for

lower computational accuracy when the power for each one of said segments is below ~~the~~a

predefined threshold.

Claim 11 (currently amended) A method according to claim 7, comprising the

additional steps of:

monitoring a computational overflow for each segment which is assigned to ~~said~~

~~adaptive filter~~ a processor for lower computational accuracy according to the computational

accuracy information; and

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ordering said controller to assign the local input signal to ~~said adaptive filter~~a

processor for higher computational accuracy when the overflow occurs in the segment.

Claim 12 (currently amended) A method according to claim 7, comprising the

additional steps of:

monitoring a computational overflow for each segment which is assigned to ~~said~~

~~adaptive filter~~a processor for lower computational accuracy according to the computational

accuracy information; and

ordering said controller to assign the local input signal to an additional ~~adaptive~~

~~filter~~processor when the overflow occurs in the segment.